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	Application No.	Applicant(s)
Notice of Allowability	09/751,679	JARVIS ET AL.
	Examiner	Art Unit
	Aimee J. Li	2183
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to After Final Amendment received 01 September 2005.		
2. X The allowed claim(s) is/are 1-7, 10-17, and 20-23 renumbered as 1-7, 8-15, and 16-19.		
3.		
Attachment(s)  1. Notice of References Cited (PTO-892)  2. Notice of Draftperson's Patent Drawing Review (PTO-948)  3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date  4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Da 8), 7. ☐ Examiner's Amenda	te

Application/Control Number: 09/751,679 Page 2

Art Unit: 2183

## REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance: The independent claims 1, 11, and 21 recite the limitations, taking claim 1 as exemplary, "...determining if the syllable in the first one of the storage locations in the first buffer has been transferred from the first buffer to the instruction pipeline; and in response to a determination that the syllable in the first one of the storage locations has been transferred, causing the corresponding syllable stored in the corresponding one of the storage locations in the second buffer to be transferred to the first one of the storage locations in the first buffer...". Claims 11 and 21 contains similar limitations. The prior art searched has taught transferring data between two buffers, e.g. from an instruction cache to an instruction buffer, but not under the specific conditions in the claims. The prior art searched did not teach that an instruction syllable in a specific location in the second buffer be transferred to the first location in the first buffer when the first buffer location is vacated. More specifically, the prior art has not taught that the second buffer has specific corresponding storage locations associated with specific storage locations in the first buffer and to transfer data from the two associated locations in the two different buffers after determining that an instruction syllable in the first buffer has been issued.

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Application/Control Number: 09/751,679

Art Unit: 2183

3. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Aimee J. Li whose telephone number is (571) 272-4169. The

examiner can normally be reached on M-T 7:00am-4:30pm.

4. If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Eddie Chan can be reached on (571) 272-4162. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

5. Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Aimee J. Li September 05

SUPERVISORY PATENT EXAMINER

Page 3

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